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Chuck Dunbar
Chuck Dunbar

Attorney's Docket No. GLO 2 0029

UTILITY PATENT APPLICATION TRANSMITTAL LETTER

(Only for non-provisional applications under 37 CFR 1.53(b))

Box PATENT APPLICATION
Assistant Commissioner for Patents
Washington, DC 20231-9999

Transmitted herewith for filing is a patent application of:

Sun, et al.

for: FLOATING CHIP PHOTONIC DEVICE AND METHOD OF MANUFACTURE

Enclosed are:

<input checked="" type="checkbox"/>	6 sheets of specification
<input checked="" type="checkbox"/>	1 sheet of Abstract
<input checked="" type="checkbox"/>	3 sheets of claims
<input checked="" type="checkbox"/>	2 sheets of drawings (Figs. 1-3)
<input checked="" type="checkbox"/>	Assignment of an invention
<input checked="" type="checkbox"/>	Declaration and Power of Attorney
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<input type="checkbox"/>	Small Entity Statement
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CLAIMS AS FILED

FOR	NO. FILED	NO. EXTRA	RATE	FEE
TOTAL CLAIMS	19	(20) -0-	<input type="checkbox"/> \$18 LARGE <input type="checkbox"/> \$9 SMALL	\$0.00
INDEPENDENT CLAIMS	3	(3) -0-	<input type="checkbox"/> \$78 LARGE <input type="checkbox"/> \$39 SMALL	\$0.00

BASIC FEE ☒ LARGE ENTITY \$690 ☐ SMALL ENTITY \$345

TOTAL FILING FEE \$690.00

- ☐ Priority is claimed from -
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3/29/00
Date

W. Scott Harders
Timothy E. Nauman
Reg. No. 32,283
W. Scott Harders
Reg. No. 42,629
Fay, Sharpe, Fagan, Minnich & McKee, LLP
1100 Superior Avenue, 7th Floor
Cleveland, OH 44114-2518
(216) 861-5582

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jc678 U.S. PTO
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FLOATING CHIP PHOTONIC DEVICE AND METHOD OF MANUFACTURE

BACKGROUND OF THE INVENTION

The present invention relates to the art of solid state lamp assemblies. It finds particular application in conjunction with Light Emitting Diodes (LED's), and will be described with particular reference thereto. It is to be appreciated, however, that the present invention is also amenable to other types of light emitting semiconductor devices.

Solid state lamp assemblies such as LEDs are typically made from a flat chip of material, such as III - V nitrides gallium arsenide, and/or phosphides and silicon carbide, suitably doped with material or designed so as to form a p-n junction which emits light when current is passed therethrough. Indeed, such devices emit light from all exposed surfaces when injected with an appropriate input current. Undesirably however, the light emitted by these devices tends to be of relatively low intensity due to scattering and/or absorption.

This inefficiency has been recognized, and effort has been directed towards solving this particular problem. Typical solutions tend to focus the light emanating from the exposed chip surfaces. Exemplary methods of implementation include angling the chip mounting area, frequently a cup, to redirect emitted light, lining the mounting area with a reflective surface, shaping the diode material itself into a lens, or adding a separate lens fixture to the LED.

The present invention contemplates a new and improved method and apparatus which helps to reduce certain of the above-referenced problems and others.

BRIEF SUMMARY OF THE INVENTION

In accordance with one aspect of the present invention, a solid state lamp includes a light emitting element and a mounting area adapted to contain the element. A suspension media supportably surrounds the element within the mounting area.

In accordance with another aspect of the present invention, the

suspension media comprises a substantially transparent material.

In accordance with another aspect of the present invention, the suspension media includes a first layer disposed between the mounting area and the light emitting diode element, and a second layer covering the light emitting diode
5 element and the first layer.

In accordance with another aspect of the present invention, the first layer includes a thermally conductive filler.

In accordance with another aspect of the present invention, the first layer includes phosphor particles.

10 In accordance with another aspect of the present invention, the second layer includes phosphor particles.

In accordance with another aspect of the present invention, a third layer is supplied and located between the light emitting diode element and the second layer.

In accordance with another aspect of the present invention, at least one
15 of the first or second layers includes gold or silver nano-particles.

In accordance with another aspect of the present invention, at least one of the first or second layers includes single crystal nano-particles such as diamonds.

In accordance with another aspect of the present invention, at least one of the first or second layers includes dielectric nano-particles such as fumed alumina,
20 TiO_2 , SrTiO_3 , etc., to improve thermal conductivity while minimizing optic scattering.

In accordance with another embodiment of the present invention, a method of manufacturing a solid state lamp includes locating a suspension media in a mounting area where a volume of the mounting area exceeds a volume of the suspension media. The method further includes disposing a light emitting device on
25 the suspension media.

In accordance with another aspect of the present invention, the method further includes affixing electrical leads to the light emitting device.

In accordance with another aspect of the present invention, the method further includes depositing a phosphor embedded suspension layer over the light
30 emitting device and the suspension media.

In accordance with another embodiment of the present invention, a photonic device includes a mounting area and a spacing element which both spaces a semiconductor device from the mounting area and affixes the semiconductor device within the mounting area.

5 In accordance with another aspect of the present invention, the spacing element comprises an optically transparent media disposed between the mounting area and the semiconductor device.

In accordance with another aspect of the present invention, the optically transparent media includes silicone epoxy blended with phosphor particles.

10 In accordance with another aspect of the present invention, the optically transparent media includes silicone epoxy blended with thermally conductive fillers.

In accordance with another aspect of the present invention, the spacing element includes an optically transparent media disposed over both the semiconductor device and the spacing element.

15 In accordance with another aspect of the present invention, the photonic device further includes a phosphor layer disposed over the semiconductor device between the spacing element and the means for affixing.

One advantage of the present invention resides in an optic buffer or spacer blended with thermally conductive fillers to improve thermal conductivity and the refractive index of the device.

20 Another advantage of the present invention resides in the ability to blend phosphor particles within the suspension media for down conversion and scattering of LED emissions.

Yet another advantage of the present invention resides in the surrounding of the LED chip by the suspension media providing a channel for the light output from all surfaces of the chip.

25 Still further advantages and benefits of the invention will become apparent to those of ordinary skill in the art upon reading and understanding the following detailed description of the preferred embodiments.

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BRIEF DESCRIPTION OF THE DRAWINGS

The invention may take form in various components and arrangements of components, and in various steps and arrangements of steps. The drawings are only for purposes of illustrating the preferred embodiments and are not to be construed as limiting the invention.

5 FIGURE 1 is a cross section of an LED photonic device in accordance with the present invention;

FIGURE 2 is a cross section of an LED photonic device according to an alternate embodiment of the present invention; and,

10 FIGURE 3 is a cross section of an LED photonic device according to an alternate embodiment of the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

With reference to Figure 1, a photonic device 10 such as the LED illustrated includes a metal mount 12 defining a mounting area or cup 14. A first layer of suspension media 16, such as silicone epoxy, is deposited into the cup 14. Those skilled in the art will appreciate that the silicone epoxy 16 can be pure or filled with additives to improve thermal conductivity, to adjust the refractive index, and/or to down convert the emission from an LED and scatter light. The viscosity of the silicone epoxy 16 is adjusted, for example by fillers or mild curing, to an extent that a deposited LED chip will stick to and sit upon the first layer 16 without settling appreciably. At this point, an LED chip 20 is placed upon the first layer 16. Either an upper face (with an electrode pad), or a bottom face (sapphire) of the LED chip 20 is usable in this configuration as a mounting face. Those skilled in the art will appreciate that if electrical leads were not previously applied to the LED chip 20, such wiring can now be bonded to the chip and to the electrode legs (not shown).

25 A second layer of silicone epoxy 22 is deposited over the exposed face of the LED chip 20 and the cured first layer 16. In the illustrated embodiment, the second layer 22 includes suspended phosphor particles blended into the silicon epoxy to improve down-conversion and scattering of LED emissions. A conventional curing process, such as thermal/UV curing, is then performed on the silicone phosphor blend.

Those skilled in the art will appreciate that conventional epoxy packaging such as forming a dome shaped epoxy onto the mounted LED, can now occur as desired.

With reference now to Figure 2, a floating chip LED 30 includes a metal mount 12 defining an LED mounting area or cup 14. A first layer of silicone epoxy suspension media 32 is blended with thermally conductive fillers 34 (enlarged for illustration) to improve thermal conductivity for higher power LED operations. In one embodiment, thermal conductivity filler 34 includes blended gold (Au) or silver (Ag) nano-particles which are commercially available. The concentration of gold/silver nano-particles is controlled to enhance the thermal conductivity without shorting the LED circuit. This blend enhances thermal conductivity while not inducing optical scattering due to the nanometer size of the particles 34. In another embodiment, the thermal conductivity filler 34 can include single crystal or dielectric nano-particles. Exemplary single crystal particles include diamonds, while dielectric particles include various oxides such as fumed alumina, TiO_2 , SrTiO_3 , etc. These nano-particles are also commercially available. These particles contribute to improved thermal conductivity while not inducing optical scattering. The resulting composite layer is still an electric insulator, but the addition of high dielectric constant additives improves the light extraction from the LED by increasing the refractive index of the silicone composite layer 32.

The device 30 also includes an LED chip 20 placed on the first layer 32. As above, wire bonding can alternately occur prior to placement, or after the first layer 32 has been sufficiently cured. In the illustrated embodiment, a phosphor layer 38 is deposited directly on the upper surface of the LED 20 and over the first layer 32. Those skilled in the art will recognize that the phosphor layer 38 acts to down convert the light emitted from the LED. A second suspension layer 40, substantially identical to the first suspension layer 32, is provided over the phosphor layer 38 to hold the LED chip 20 in place, and improve thermal conductivity by the addition of thermal conductivity fillers 34.

With reference now to Figure 3, an alternate embodiment is illustrated which also increases the surface area from which an LED chip 20' emits light. Substantially as above, a first layer 16 is deposited into an LED cup or lead frame 14.

The first layer 16 is then at least partially cured so that it will support the LED chip 20' within the cup 14. While the chip 20' is illustrated as being fully suspended within the cup 14, those skilled in the art will appreciate that the chip 20' can alternately rest on the base of the cup while still providing enhanced efficiency of emitted light. A second
5 layer 22 is then deposited over both the LED chip 20' and the first layer 16 to further support the chip 20' and electrical leads 50. As above, the suspension media 16, 22 may be dispersed with additives to improve thermal efficiency and/or quality of output light.

The invention has been described with reference to the preferred
10 embodiments. Modifications and alterations will occur to others upon a reading and understanding of the preceding detailed description. It is intended that the invention be construed as including all such modifications and alterations insofar as they come within the scope of the appended claims or the equivalents thereof.

Having thus described the preferred embodiments, the invention is claimed to be:

1. A solid state lamp comprising
5 a light emitting element;
a mounting area adapted to contain the light emitting element; and,
a suspension media disposed between said mounting area and said light emitting element which supportably surrounds the light emitting element within the mounting area.
- 10 2. The solid state lamp as set forth in claim 1, wherein the suspension media comprises a substantially optically transparent material.
3. The solid state lamp as set forth in claim 1, wherein the suspension
15 media includes a first layer disposed between the mounting area and the light emitting element, the suspension media further comprising:
a second layer covering the light emitting element and the first layer.
- 20 4. The solid state lamp as set forth in claim 3, wherein selected ones of the first layer and the second layer comprise a thermally conductive filler.
5. The solid state lamp as set forth in claim 3, wherein selected ones of the first layer and the second layer comprise phosphor.
- 25 6. The solid state lamp as set forth in claim 3, further comprising a third layer disposed between the light emitting diode element and the second layer.
7. The solid state lamp as set forth in claim 4, wherein the thermally
30 conductive filler comprises at least one nano-particle selected from the set of gold and silver.

8. The solid state lamp as set forth in claim 3, wherein at least one of the first layer and the second layer comprise single crystal nano-particles.

9. The solid state lamp as set forth in claim 3, wherein at least one of the first layer and the second layer comprise dielectric nano-particles.

10. A method of manufacturing a solid state lamp comprising:
locating a suspension media in a mounting area, where a volume of the mounting area exceeds a volume of the suspension media;
at least partially curing the suspension media; and,
supporting a light emitting device with the suspension media.

11. The method of manufacturing a solid state lamp as set forth in claim 10, further comprising affixing electrical leads to the light emitting device.

12. The method of manufacturing a solid state lamp as set forth in claim 11, further comprising depositing a phosphor layer over the light emitting device.

13. The method of manufacturing a solid state lamp as set forth in claim 11, further comprising depositing a phosphor embedded suspension layer over the light emitting device and the suspension media.

14. A photonic device comprising:
a mounting area;
a spacing element which spaces a semiconductor device from the mounting area; and,
means for affixing the semiconductor device substantially within the mounting area.

15. The photonic device as set forth in claim 14, where the spacing element includes an optically transparent media disposed between the mounting area and the semiconductor device.

5 16. The photonic device as set forth in claim 15, where the optically transparent media disposed between the mounting area and the semiconductor device comprises silicone epoxy blended with phosphor particles.

10 17. The photonic device as set forth in claim 15, where the optically transparent media disposed between the mounting area and the semiconductor device comprises silicone epoxy blended with thermally conductive fillers.

15 18. The photonic device as set forth in claim 14, where the affixing the semiconductor device substantially within the mounting area includes an optically transparent media disposed over the semiconductor device and the spacing element.

20 19. The photonic device as set forth in claim 18 further comprising a phosphor layer disposed over the semiconductor device between the spacing element and the means for affixing the semiconductor device.

RD 27695
GLO 2 0029

FLOATING CHIP PHOTONIC DEVICE AND
METHOD OF MANUFACTURE

ABSTRACT OF THE DISCLOSURE

5 A solid state lamp includes a mounting area adapted to contain a light emitting diode (LED) chip and a suspension media which physically isolates the diode from the mounting area. The suspension media, while substantially optically transparent, includes suspended phosphor particles for down conversion and scattering of LED emissions. Additionally, the suspension media includes thermal conductivity additives to improve device thermal conductivity in higher power operations.

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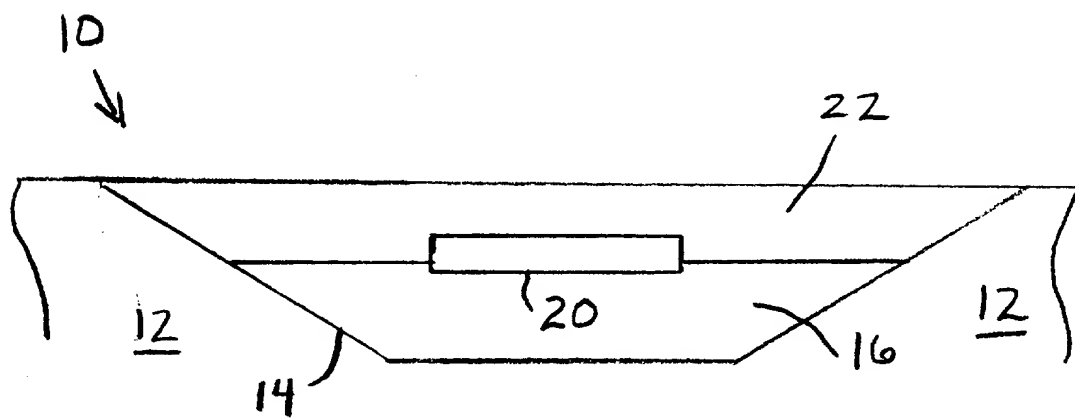


FIG. 1

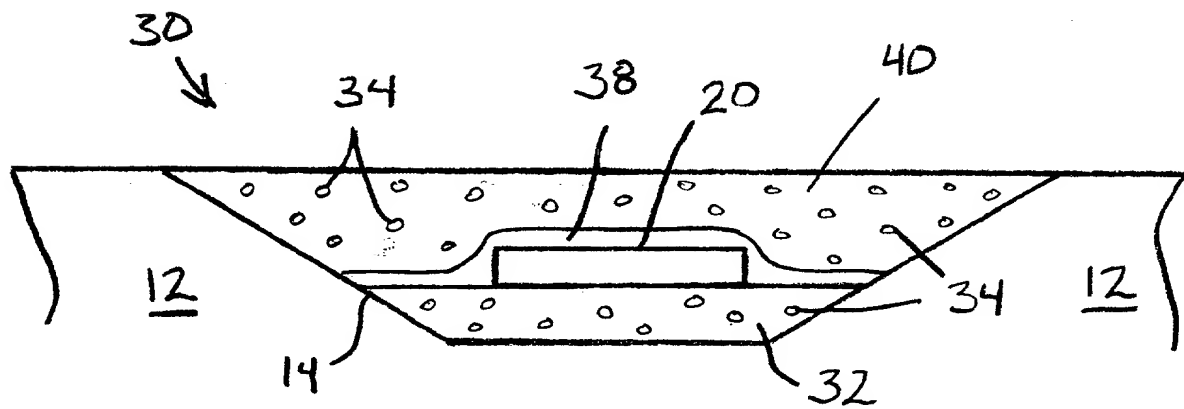


FIG. 2

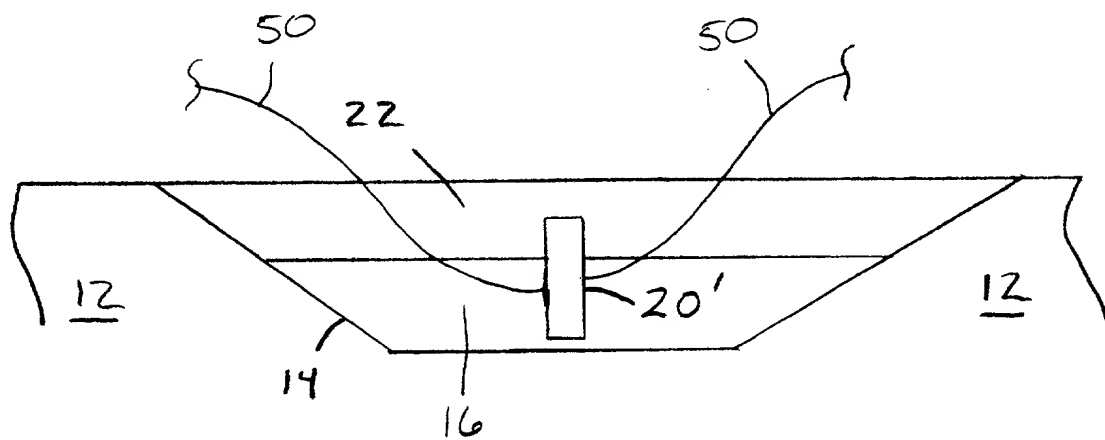


FIG. 3

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(GLO 2 0029)
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DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

As a below inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled:

FLOATING CHIP PHOTONIC DEVICE AND METHOD OF MANUFACTURE

the specification of which:

X is attached hereto OR
_____ was filed on _____ as U.S. Serial No. _____.

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, §1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)
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I hereby claim the benefit under Title 35, United States Code, §119(a) of any United States provisional application(s) listed below:

_____ (Serial No.)	_____ (Day/Month/Year Filed)
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I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) or any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application or PCT International application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information which is material to patentability as defined in Title 37, of Federal Regulations Code, §1.56(a) which became available between the filing date of the prior application and the national or PCT international filing date of this application:

_____ (Application Serial No.)	_____ (Filing Date)
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Status: _____
(Patented, Pending, Abandoned)

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(GLO 2 0029)
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POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith:

Li-Hua Luo, Reg. No. 37,986;
Ronald E. Myrick, Reg. No. 26,315;
Henry J. Policinski, Reg. No. 16,621;

Steven M. Auvil, Reg. No. 40,492
Mark E. Bandy, Reg. No. 35,798
Brian G. Benbenick, Reg. No. 41,463
Joseph D. Dreher, Reg. No. 37,123
Christopher B. Fagan, Reg. No. 22,987
Jude A. Fry, Reg. No. 38,340
Steven M. Haas, Reg. No. 37,841
Michael E. Hudzinski, Reg. No. 34,185
Richard M. Klein, Reg. No. 33,000
Thomas E. Kosevsky Jr., Reg. No. 28,383
Sandza M. Koenig, Reg. No. 33,722

Brian E. Kondas, Reg. No. 40,685
Scott A. McCollister, Reg. No. 33,961
James W. McKee, Reg. No. 26,462
Richard J. Minnich, Reg. No. 24,175
Jay F. Moldovanyi, Reg. No. 29,678
Philip J. May, Reg. No. 31,260
Timothy S. Nauman, Reg. No. 32,283
Patrick R. Roche, Reg. No. 29,580
Albert P. Sharpe, III, Reg. No. 19,879
Ann M. Skeer, Reg. No. F 46,655
R. Scott Speroff, Reg. No. 37,450
Mark S. Svatek, Reg. No. 34,261

SEND CORRESPONDENCE TO:
Scott A. McCollister, Esq.
Fay, Sharpe, Fagan,
Minnich & McKee, LLP, 7th Floor
1100 Superior Avenue
Cleveland, Ohio 44114-2518

DIRECT TELEPHONE CALLS TO:
(name and telephone number)

Scott A. McCollister, Esq.
(216) 861-5582

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both under §1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Full name of first joint (or sole) inventor: Xiao-Dong Sun

Inventor's Signature: [Signature]

Date: 3/29/00

Residence: Schenectady, New York

Country of Citizenship: CHINA P.R.

Post Office Address: 1455 Dorwaldt Blvd, B9A12
Schenectady, NY 12308

Full name of second joint inventor: William P. Minnear

Inventor's Signature: [Signature]

Date: 3/29/00

Residence: Clifton Park, New York

Country of Citizenship: U.S.A.

Post Office Address: 16 Timberwick
Clifton Park, NY 12065

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